

IN THE CLAIMS

1. (Currently Amended) A display device comprising:
 - a display area on a substrate;
 - at least one power bus line of a first type;
 - at least one power bus line of a second type;
 - a cathode layer coupled to display elements in the display area and to the power bus line of the first type through a first interconnect;
 - an insulating layer formed on the cathode; and
 - a power supply plane formed on the insulating layer connected to the power bus line of the second type through a second interconnect,wherein the insulating layer separates the power supply plane from the cathode, and wherein the power supply plane ~~provide~~ provides an even distribution of power to the EL elements.
2. (Currently Amended) The device of claim 1 wherein the power bus line of the first type is a negative ~~power~~ voltage supply.
3. (Currently Amended) The device of claim 1 wherein the power bus line of the second type is a positive ~~power~~ voltage supply.
4. (Original) The device of claim 1 further comprising one or more power supply lines coupled to the power supply plane for providing power to the display elements.

5. (Currently Amended) The device of claim 4 wherein at least one of the first or the second interconnect is ~~one or more interconnections~~ are made at different sides of the display area for connecting the power supply plane to the power supply lines.
6. (Currently Amended) The device of claim 5 wherein the first and the second interconnects ~~interconnections~~ are made at opposite sides of the display area.
7. (Currently Amended) The device of claim 5 wherein the first and the second interconnects ~~interconnections~~ are made at adjacent sides of the display area.
8. (Original) The device of claim 1 wherein the power supply plane connects to an anode of each display element through a via connection.
9. (Currently Amended) An organic light emission display device comprising:
a cathode layer;
an insulating layer covering at least one portion of the cathode layer; and
a power supply plane formed on the insulating layer overlapping the covered portion of the cathode layer to form a predetermined area under which a display area is formed,
wherein the power supply plane provides even distribution of power to the display area through one or more via interconnects which electromagnetically couple the power supply plane to the display area.
10. (Currently Amended) The device of claim 9 wherein the power supply plane connects to one or more power supply lines through the one or more via interconnections.

11. (Currently Amended) The device of claim 10 wherein the via interconnections are made at two ends of the power supply lines.

12. (Original) The device of claim 10 wherein the power supply lines are in a mesh form.

13. (Currently Amended) The device of claim 10 wherein the via interconnections are formed in areas of the power supply plane that do not overlap with the insulating layer.

14. (Currently Amended) The device of claim 9 further comprising a negative ~~power~~ voltage supply coupled to the cathode layer.

15. (Currently Amended) A method for forming power supply of a light emission display device, the method comprising:

forming a cathode layer;

forming an insulating layer covering at least one portion of the cathode layer; and

forming a power supply plane on the insulating layer overlapping the covered portion of the cathode layer to form a predetermined area under which a display area is located, and

wherein the power supply plane provides a substantially even distribution of power to the active display area through at least one or more via interconnections.

16. (Currently Amended) The method of claim 15 further comprising forming one or more interconnections for connecting the power supply plane to one or more power

supply lines that provide positive ~~power~~ voltage to one or more display element in the display area.

17. (Original) The method of claim 16 wherein the interconnections are formed at two ends of each power supply line.

18. (Original) The method of claim 16 wherein the power supply lines are in a mesh form.

19. (Original) The method of claim 16 wherein the interconnections are formed in areas of the power supply plane that do not overlap with the insulating layer.

20. (Original) The method of claim 15 further comprising forming one or more interconnections for connecting the cathode layer to at least one power bus line in an area that do not overlap with the insulating layer.

21. (New) A display device comprising a display area on a substrate; at least one power bus line of a first type; at least one power bus line of a second type; a cathode layer coupled to display elements in the display area and the power bus line of the first type; an insulating layer formed on the cathode; and a power supply plane formed on the insulating layer connected to the power bus line of the second type, wherein the insulating layer separates the power supply plane from the cathode, and wherein the power supply plane provide even distribution of power to the EL elements; and wherein the power supply plane connects to an anode of each display element through a via connection.

22. (New) An organic light emission display device comprising a cathode layer; an insulating layer covering at least one portion of the cathode layer; and a power supply plane formed on the insulating layer overlapping the covered portion of the cathode layer to form a predetermined area under which a display area is formed, wherein the power supply plane provides even distribution of power to the display area, and wherein the power supply plane connects to one or more power supply lines through one or more interconnections where the interconnections are made at two ends of the power supply lines.

23. (New) A method for forming power supply of a light emission display device, the method comprising:

forming a cathode layer;

forming an insulating layer covering at least one portion of the cathode layer;

forming a power supply plane on the insulating layer overlapping the covered portion of the cathode layer to form a predetermined area under which a display area is located, and

forming one or more interconnections for connecting the power supply plane to one or more power supply lines that provide positive voltage to one or more display element in the display area, wherein the interconnections are formed at two ends of each power supply line;

wherein the power supply plane provides even distribution of power to the active display area.